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October 7, 2002

Via Facsimile 703.872.9318

Commissioner for Patents
Washington, D.C. 20231

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OCT 07 2002

TECHNOLOGY CENTER 2800

Re: Serial No.: 09/738,193
Art Unit: 2827
Examiner: David E. Graybill
System, Method and Apparatus for Constructing a Semiconductor Wafer-Interposer Using B-Stage Laminates
Our File: 1384-1023


Dear Sir:

Enclosed for filing please find the following items relating (9 pages) to the above-identified application:

- (1) Response Under 37 C.F.R. §1.111 (4 pages);
- (2) Petition Under 37 C.F.R. §1.136 for a One Month Extension of Time (1 page);
- (3) Form PTO-2038 authorizing a payment in the amount of \$95.00 (1 page);
- (4) Assignment Recordation Form (PTO-1595) with Assignment (2 pages); and
- (5) Change of Attorney Correspondence Address (1 page).

If you have any questions or comments concerning this matter, please call the undersigned at your earliest convenience. Otherwise, please accept the enclosed.


Sincerely,


Lawrence R. Youst
Reg. No. 38,795

Enclosures

Certificate of Transmission Under 37 C.F.R. §1.8

I hereby certify that this correspondence is being transmitted by facsimile to the United States Patent and Trademark Office on October 7, 2002.


Lawrence R. Youst

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